

**FIGURE 1**



TITLE: PIN-OUT CONNECTIONS/DRIVE LEVELS DIRECT SET BY DROP DOWN LIST

Inventor(s): Kenneth Y. Ogami and Douglas H. Anderson

USSN: 10/032,986

Attorney Docket #: CYPR-CD01173M

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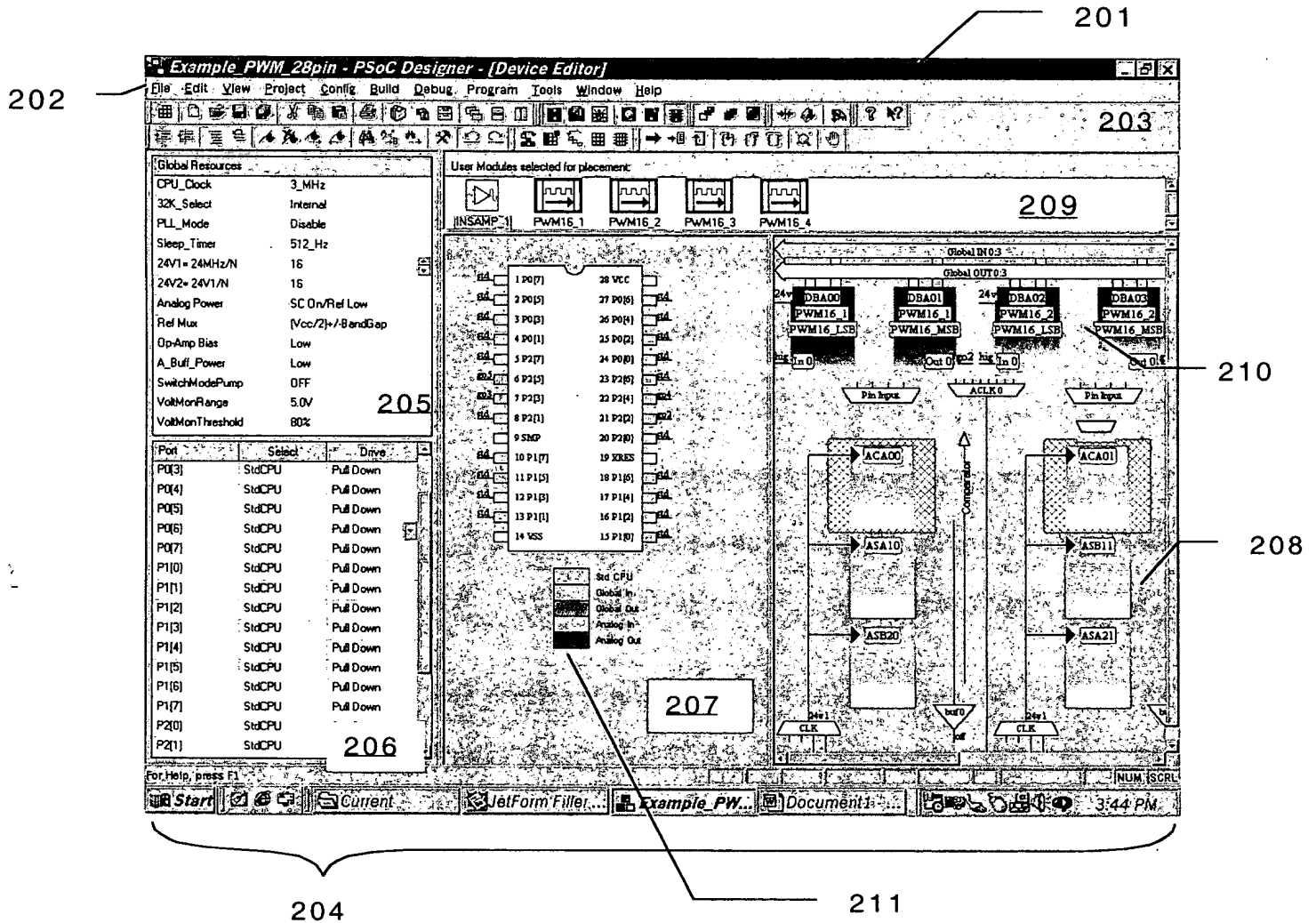


FIGURE 2

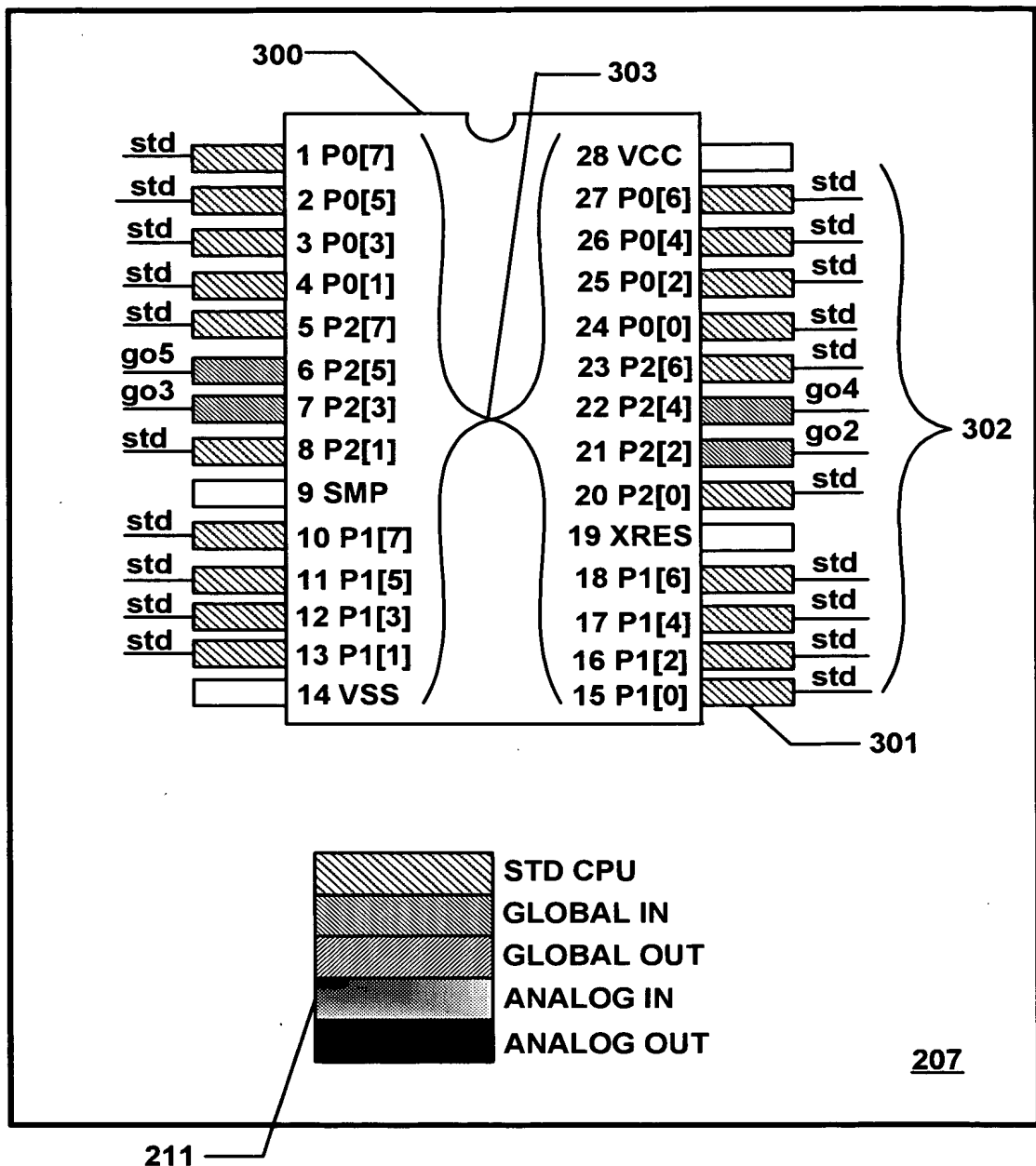


FIGURE 3

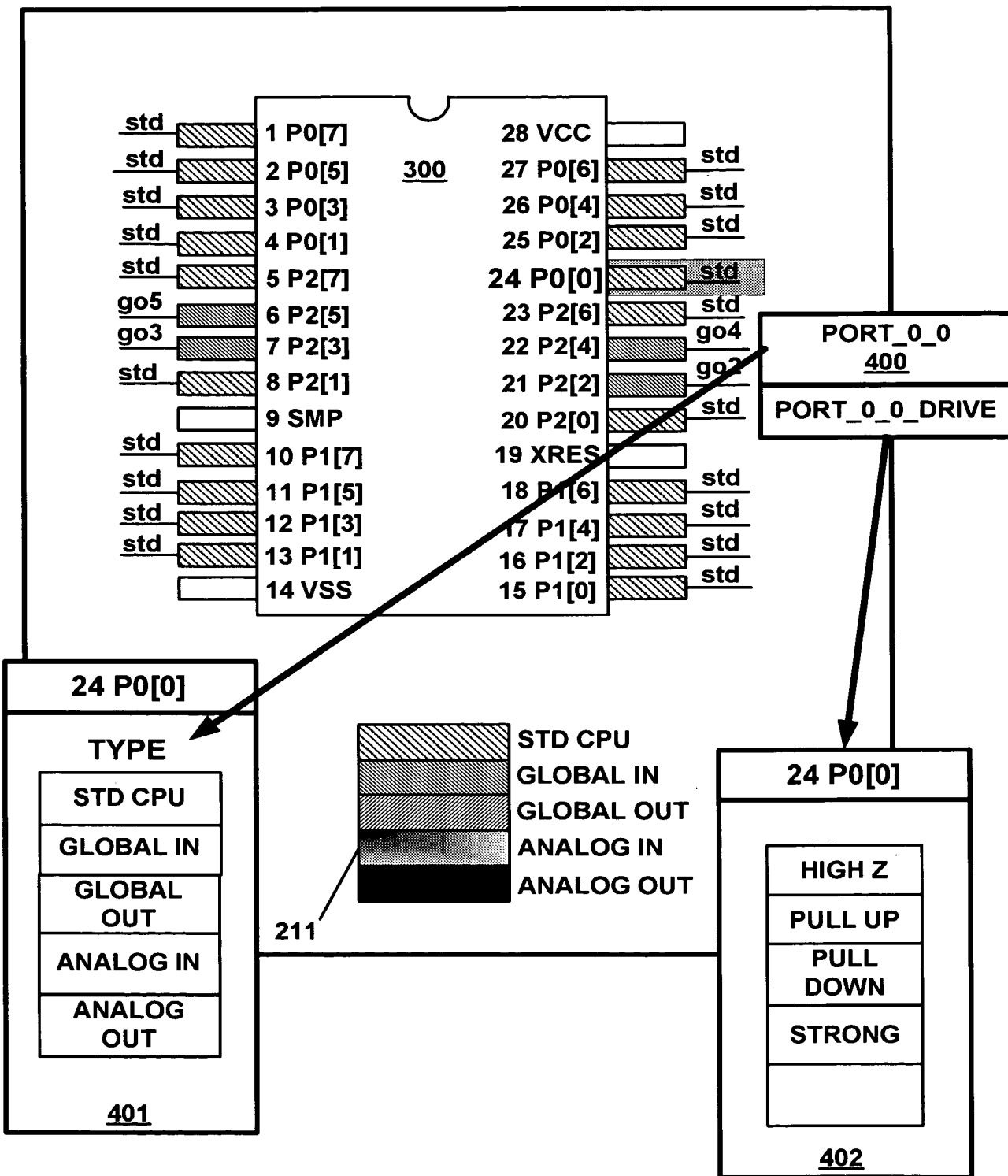


FIGURE 4



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501

501			506
PORT	SELECT	DRIVE	
P0 [0]	StdCPU	HIGH Z	506
P0 [1]	StdCPU	PULL DOWN	
P0 [2]	StdCPU	PULL DOWN	505
P0 [3]	StdCPU	PULL DOWN	
P0 [4]	StdCPU	PULL DOWN	504
P0 [5]	StdCPU	PULL DOWN	
P0 [6]	StdCPU	PULL DOWN	503
P0 [7]	StdCPU	PULL DOWN	
P1 [0]	StdCPU	PULL DOWN	505
P1 [1]	StdCPU	PULL DOWN	
P1 [2]	StdCPU	PULL DOWN	504
P1 [3]	StdCPU	PULL DOWN	
P1 [4]	StdCPU	PULL DOWN	503
P1 [5]	StdCPU	PULL DOWN	
P1 [6]	StdCPU	PULL DOWN	505
P1 [7]	StdCPU	PULL DOWN	
P2 [0]	StdCPU	PULL DOWN	504
P2 [1]	GLOBAL OUT	PULL DOWN	
P2 [2]	GLOBAL OUT	PULL DOWN	503
P2 [3]	GLOBAL OUT	PULL DOWN	

502

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FIGURE 5

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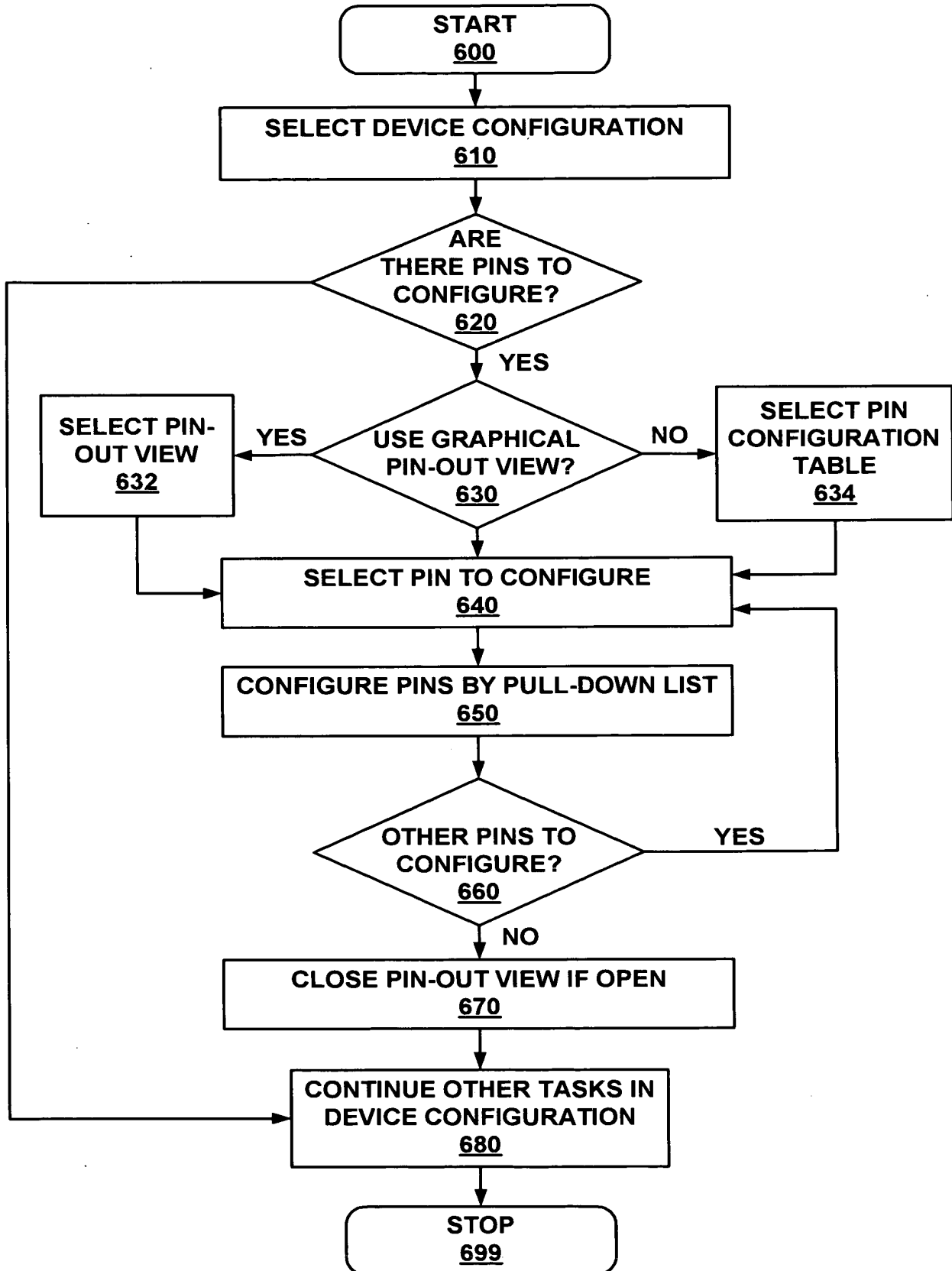


FIGURE 6